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CONFIRMATION NO. 9232

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APPLICANTS

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** CONTINUING DATA ***************************
This application is a CON of 10/253,354 09/23/2002 PAT 6,780,711
which is a CON of 09/939,431 08/24/2001 PAT 6,483,736
which is a CON of 09/714,440 11/15/2000 PAT 6,351,406
which is a CON of 09/469,658 12/22/1999 PAT 6,185,122
which is a DIV of 09/192,883 11/16/1998 PAT 6,034,882

yes, TL

** FOREIGN APPLICATIONS ***********************
None, TL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 05/08/2004

Foreign Priority claimed ☐ yes ☑ no	STATE OR COUNTRY CA	SHEETS DRAWING 24	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met ☐ yes ☑ no ☐ Met after Allowance				
Verified and Acknowledged ___Examiner's Signature___ ___Initials___				

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TITLE
Integrated circuit incorporating three-dimensional memory array with dual opposing decoder arrangement

FILING FEE RECEIVED 1332	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	☐ All Fees
		☐ 1.16 Fees (Filing)
		☐ 1.17 Fees (Processing Ext. of time)
		☐ 1.18 Fees (Issue)
		☐ Other _____
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